

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Mikio Oda, et al.

Examiner: Thien F. Tran

Serial No.: 10/584,736

Art Unit: 2895

Filed: June 26, 2006

Docket: 19992

For: OPTICAL INPUT SUBSTRATE,
OPTICAL OUTPUT SUBSTRATE, OPTICAL
INPUT/OUTPUT SUBSTRATE, A FABRICATION METHOD
FOR THESE SUBSTRATES, AND AN OPTICAL ELEMENT
INTEGRATED SEMICONDUCTOR INTEGRATED CIRCUIT

Confirmation No. 8208

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO REQUIREMENT FOR RESTRICTION

Sir:

Pursuant to the Restriction Requirement imposed in the Official Action dated March 25, 2009, applicants provisionally elect the claims of Group I, i.e., Claims 1-20 for continued prosecution herein.

Claims 1-34 are present in the above-captioned application and have been subjected to restriction under 35 U.S.C. §121. Specifically, the Official Action avers that the following inventions are present in the claims:

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on April 21, 2009.

Dated: April 21, 2009



Paul J. Esatto, Jr.

Group I, Claims 1-20, drawn to an optical substrate.

Group II, Claims 21-34, drawn to a process of making an optical substrate.

It is the Examiner's position that the inventions listed as Groups I and II are distinct from each other.

In response to the Examiner's requirement for restriction, applicants provisionally elect to prosecute the subject matter of Group I, Claims 1-20. However, applicants reserve the right under 35 U.S.C. §121 to file one or more divisional applications directed to the non-elected claims in this application.

In view of the foregoing, an examination on the merits of the elected claims, at an early date, is earnestly solicited.

Respectfully submitted,



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